

TO/2/06/27/01
10000 US 8093023 PTO
JC639 U.S.
Patent Application of: Frank O'Mahony et al.

4
10000 US 8093023 PTO
JC639 U.S.
LOW LOSS INTERCONNECT STRUCTURE FOR USE IN MICROELECTRONIC CIRCUITS

Attorney Docket No.: 884.405US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents
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- Return postcard.
 Utility Patent Application under 37 CFR § 1.53(b) comprising:
 Specification (13 pgs, including claims numbered 1 through 25 and a 1 page Abstract).
 Formal Drawing(s) (4 sheets).
 Signed Combined Declaration and Power of Attorney (8 pgs).
 Check in the amount of \$800.00 to pay the filing fee.
 Assignment of the invention to Intel Corporation (4 pgs) and Recordation Form Cover Sheet.
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TOTAL CLAIMS	25 - 20 =	5	x 18 =	\$90.00
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